

Compositional non-interference

for fine-grained concurrent programs

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- Confidentiality: secret information is not revealed to an attacker.
- Non-interference: varying the secret information does not lead to observably different behavior.

Language-based approach: When an imperative program is secure?

All the variables are divided into two groups.

- *low-sensitivity* variables l_1, l_2, \dots ,
- and *high-sensitivity* variables h_1, h_2, \dots .

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- and *high-sensitivity* variables h_1, h_2, \dots .

Following this:

- Confidentiality: the data stored in high-sensitivity variables should not leak to low-sensitivity variables.
- Non-interference: changing the values of h_1, h_2, \dots and running the program does not affect the resulting values of l_1, l_2, \dots .
- Preventing information leaks, e.g., $l_1 \leftarrow !h_1 + 1$.

Type systems for non-interference

Type system where types are annotated with labels from a lattice $\mathbf{L} \sqsubseteq \mathbf{H}$.

$$\vdash l_j : \text{ref int}^{\mathbf{L}}$$
$$\vdash h_j : \text{ref int}^{\mathbf{H}}$$
$$\frac{\vdash e : \text{ref int}^{\chi} \quad \vdash t : \text{int}^{\xi} \quad \xi \sqsubseteq \chi}{\vdash e \leftarrow t : \text{unit}}$$
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Example:

$$\frac{\vdash l_1 : \text{ref int}^{\mathbf{L}} \quad \frac{\vdash !h_1 : \text{int}^{\mathbf{H}} \quad \vdash 1 : \text{int}^{\mathbf{L}}}{\vdash !h_1 + 1 : \text{int}^{\mathbf{H}}}}{\vdash l_1 \leftarrow !h_1 + 1 : \text{unit}} \quad \mathbf{H} \sqsubseteq \mathbf{L}$$

- Can be extended to cover more PL features (dynamic references, higher-order functions, exceptions), although it is not straightforward.
- Type systems are too weak: in many situations non-interference depends on functional correctness.

Example: value-dependent classifications

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- Can we type this program with conventional type systems? *No*
- Is this program secure? *Yes*

Solution: semantic typing & program logic

- We give a relational extension of Iris for reasoning about non-interference
- We model a type system using logical relations
- We prove soundness w.r.t. scheduler-independent notion of non-interference

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Solution: semantic typing & program logic

- We give a relational extension of Iris for reasoning about non-interference
 - Enables reasoning about fine-grained concurrency
 - Enables reasoning about functional correctness
- We model a type system using logical relations
 - Compatibility rules for composing typed programs
 - Can “drop down” to the model to prove semantic typing manually
- We prove soundness w.r.t. scheduler-independent notion of non-interference

The logic

Double weakest precondition

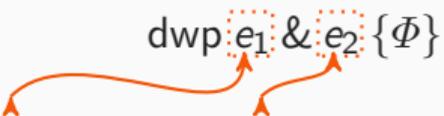
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- e_1 and e_2 have different secret data, but must produce the same public output.
- Left-hand side and right-hand side resources: $\ell_1 \mapsto_L v_1$ and $\ell_2 \mapsto_R v_2$.
- Soundness statement:

$$(\forall h_1, h_2 \in \mathbb{Z}. I_{out} \vdash \text{dwp } e[h_1/x] \ \& \ e[h_2/x] \ \{ v_1 v_2. v_1 = v_2 \}) \implies e \text{ is secure}$$

$$I_{out} \triangleq \boxed{\exists v \in \mathbb{Z}. out \mapsto_L v * out \mapsto_R v}$$

Example: value-dependent classifications

Let *prog secret out* be

$$\text{let } r = \left\{ \begin{array}{l} \text{data} = \text{ref}(\text{secret}); \\ \text{is_classified} = \text{ref}(\text{true}) \end{array} \right\} \text{ in}$$

<i>while</i> true <i>do</i>		
if $\neg ! r.\text{is_classified}$		<i>r.data</i> $\leftarrow 0$;
then <i>out</i> $\leftarrow ! r.\text{data}$		<i>r.is_classified</i> \leftarrow false
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Statement we want to prove:

$$\forall h_1, h_2. I_{out} \vdash \text{dwp } \text{prog } h_1 \text{ out} \ \& \ \text{prog } h_2 \text{ out} \ \{v_1 v_2. v_1 = v_2\}$$
$$I_{out} \triangleq \boxed{\exists v \in \mathbb{Z}. \text{out} \mapsto_L v * \text{out} \mapsto_R v}$$

Proof rules

$$\frac{e_1 \rightarrow_{\text{pure}} e'_1 \quad e_2 \rightarrow_{\text{pure}} e'_2 \quad \triangleright \text{dwp } e'_1 \ \& \ e'_2 \ \{\Phi\}}{\text{dwp } e_1 \ \& \ e_2 \ \{\Phi\}} \quad \frac{\text{dwp } e_1 \ \& \ e_2 \ \{v_1 \ v_2. \text{dwp } K_1[v_1] \ \& \ K_2[v_2] \ \{\Phi\}\}}{\text{dwp } K_1[e_1] \ \& \ K_2[e_2] \ \{\Phi\}}$$

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for atomic e_1, e_2 that do not fork off new threads

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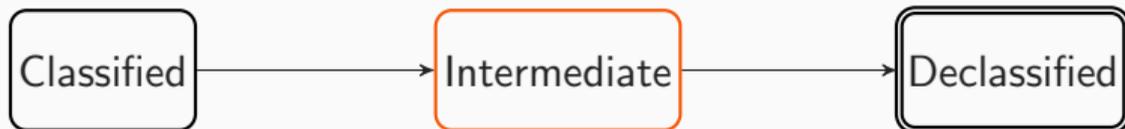
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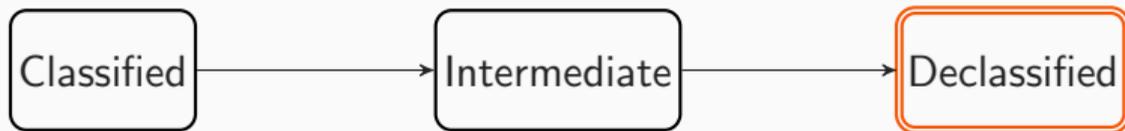
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$$\begin{aligned} & (\text{in_state}(\text{Classified}) * \exists i_1, i_2. r_1.\text{is_classified} \mapsto_L \mathbf{true} * \\ & \quad r_2.\text{is_classified} \mapsto_R \mathbf{true} * r_1.\text{data} \mapsto_L i_1 * r_2.\text{data} \mapsto_R i_2) \\ \vee & (\text{in_state}(\text{Intermediate}) * \exists i. r_1.\text{is_classified} \mapsto_L \mathbf{true} * \\ & \quad r_2.\text{is_classified} \mapsto_R \mathbf{true} * r_1.\text{data} \mapsto_L i * r_2.\text{data} \mapsto_R i) \\ \vee & (\text{in_state}(\text{Declassified}) * \exists i. r_1.\text{is_classified} \mapsto_L \mathbf{false} * \\ & \quad r_2.\text{is_classified} \mapsto_R \mathbf{false} * r_1.\text{data} \mapsto_L i * r_2.\text{data} \mapsto_R i) \end{aligned}$$


Typing

We build a type system as an abstraction on top of the logic.

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Grammar of types:

$$\tau \in \text{Type} ::= \text{unit} \mid \text{int}^x \mid \text{bool}^x \mid \tau \times \tau' \mid \text{ref } \tau \mid (\tau \rightarrow \tau')^x$$

Typing judgements:

$$\Gamma \vdash e : \tau$$

We follow the usual structure of logical relations in Iris: from $\Gamma \vdash e : \tau$ to $\Gamma \models e : \tau$.

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$$\llbracket \Gamma \rrbracket(\gamma) \triangleq \forall x. \llbracket \Gamma(x) \rrbracket(\gamma_1(x), \gamma_2(x))$$

Compatibility lemmas:

$$\frac{\vdash e : \text{ref } \tau}{\vdash !e : \tau}$$

$$\frac{\text{dwp } e \& e' \{ \llbracket \text{ref } \tau \rrbracket \}}{\text{dwp } !e \& !e' \{ \llbracket \tau \rrbracket \}}$$

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Fundamental property:

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Soundness:

$$x : \text{int}^{\mathbf{H}} \vdash e : \text{int}^{\mathbf{L}} \implies e \text{ is secure}$$

We prove the soundness of the type system using the soundness of dwp.

Soundness

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We take one of the standard definitions of non-interference for concurrent programs:

$$e \text{ is secure} \triangleq \forall h_1 h_2. e[h_1/x] \cong_L e[h_2/x]$$

$e_1 \cong_L e_2 \triangleq$ for any σ s.t. $\sigma(out) \in \mathbb{Z}$, the configurations (e_1, σ) and (e_2, σ) are related by a *strong-low bisimulation* (Sabelfeld & Sands, 2000).

Strong-low bisimulation is a partial equivalence relation \mathcal{R} on configurations such that

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- The bisimulation condition holds:

$$\begin{array}{ccccc} (e_i, \sigma_1) & & (e_0 \dots e_i \dots, \sigma_1) & \text{--- } \mathcal{R} \text{ ---} & (t_0 \dots t_i \dots, \sigma_2) & & (t_i, \sigma_2) \\ & & \downarrow & & \downarrow & & \\ (e'_i \vec{e}, \sigma'_1) & & (e_0 \dots e'_i \vec{e} \dots, \sigma'_1) & & & & \end{array}$$

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 \end{array}$$

Constructing the bisimulation

The specific bisimulation that we construct is \mathcal{R}^* where

$(e_0 e_1 \dots e_m, \sigma_1) \mathcal{R} (t_0 t_1 \dots t_m, \sigma_2) \triangleq$ there exists n such that

$$\text{True} \vdash \left(\top \Vdash^\emptyset \triangleright \emptyset \Vdash^\top \right)^n \Vdash_{\top} SR(\sigma_1, \sigma_2) * I_{out} *$$

$$\text{dwp } e_0 \ \& \ t_0 \ \{v_1 \ v_2. \ v_1 = v_2\} *$$

$$*_{1 \leq i \leq m}. \text{dwp } e_i \ \& \ t_i \ \{\text{True}\}$$

Proof that \mathcal{R}^* is a bisimulation relies on the soundness of the update modality in Iris.

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We get

$$(I_{out} \vdash \text{dwp } e_1 \ \& \ e_2 \ \{v_1 \ v_2. v_1 = v_2\}) \implies (e_1, \sigma) \mathcal{R}^* (e_2, \sigma)$$

Deliberate information release

Problem:

- “Vanilla” non-interference does not allow information flow high-sensitivity input to low-sensitivity output at all.
- In many real-world situation we want to permit *some* information leakage in a controlled way.

We can encode a specific form of deliberate information release in SeLoC.

Example: Alice's calendar

Consider the following example (Constanzo & Shao, 2014):

```
let cal A = iter ( $\lambda$  i v. if (v == 0) then print(i)) A
```

- the input list A represents a calendar of Alice
- $A[i]$ contains information about the i -th day:
 - $A[i] = 0 \implies$ Alice is free on that day;
 - $A[i] = t \implies$ Alice has a meeting scheduled at time t .
- Alice wants to share the days when she is free, but she does *not* want to share the exact times she is busy

Specification of the example: Alice's calendar

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$$\forall A_1, A_2. A_1 \sim A_2 \vdash \text{dwp } \text{cal } A_1 \ \& \ \text{cal } A_2 \ \{v_1 \ v_2. v_1 = v_2\}$$

where $A_1 \sim A_2 \triangleq |A_1| = |A_2| \wedge \forall i. A_1[i] = 0 \iff A_2[i] = 0$.

The relation \sim on the calendars can be generalized to arbitrary PERs on values (Sabelfeld & Meyers 2003) and we can readily do that in SeLoC.

Further work:

- Formalizing the soundness statement.
- Integrating into the type system.
- Controlling *where* and *when* the information is released (e.g., the information is leaked only after the password is entered).

SeLoC: logic for proving non-interference of fine-grained concurrent programs.

- Formalized in Iris in Coq.
- A model of an IFC type system with semantic typing.
- Modular HOCAP-style dwp specifications.

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Thank you for your attention.

More information: <https://arxiv.org/abs/1910.00905>.