



# The seL4 microkernel verification

QED+20, Vienna

Gerwin Klein



Australian Government  
Department of Broadband, Communications  
and the Digital Economy  
Australian Research Council

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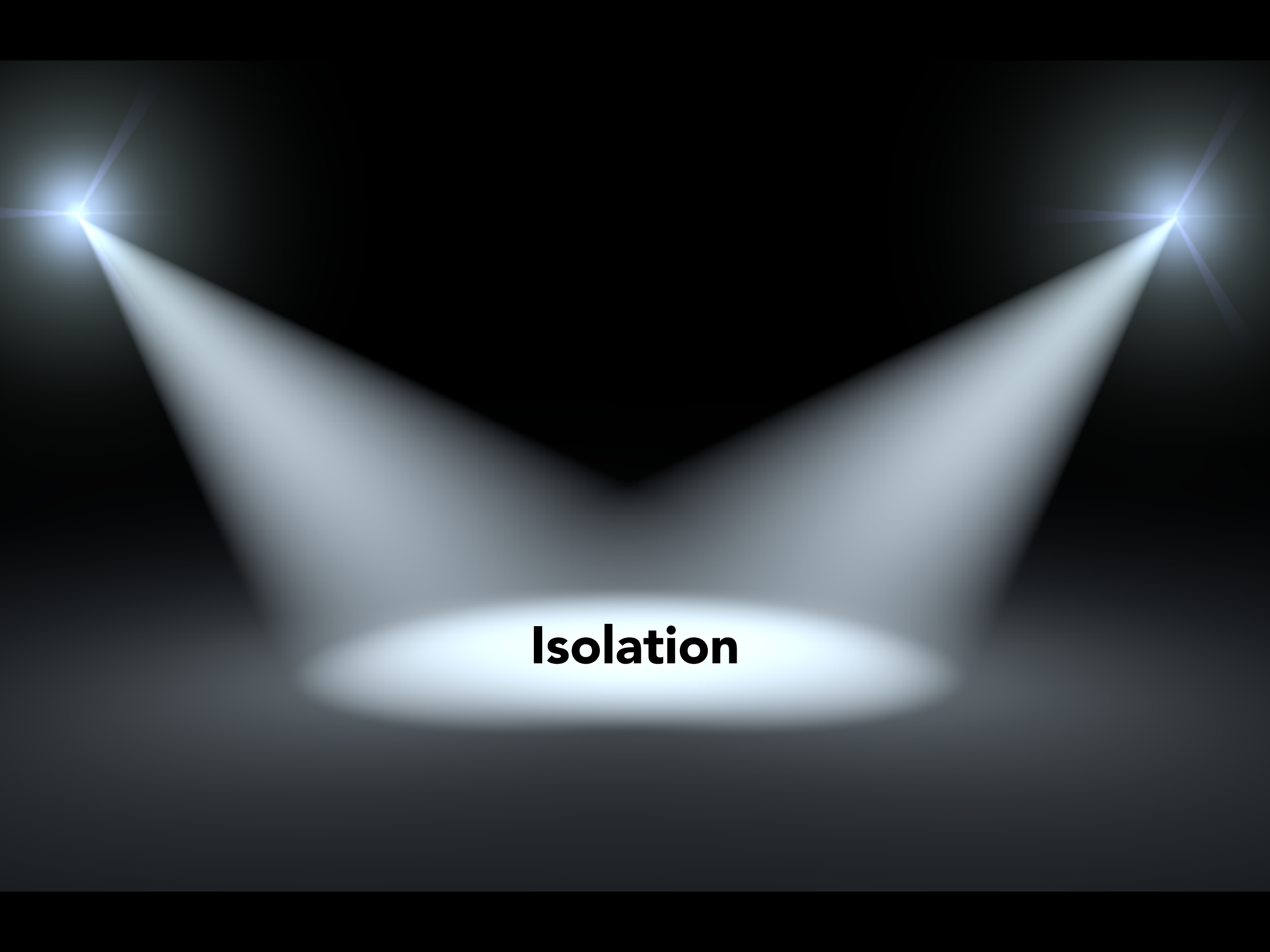


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AUSTRALIA



The image features a dark, almost black, background. Two bright spotlights are positioned at the top left and top right corners. Their beams of light converge towards the center, creating a large, bright, oval-shaped pool of light on the floor. The word "Isolation" is written in a bold, black, sans-serif font, centered within this illuminated area. The overall mood is dramatic and somber.

**Isolation**

# Isolation is the Key

## Trustworthy Computing Base

- message passing
- virtual memory
- interrupt handling
- access control

## Applications

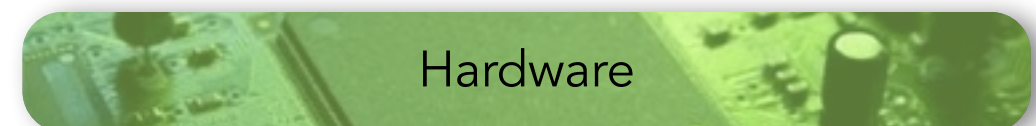
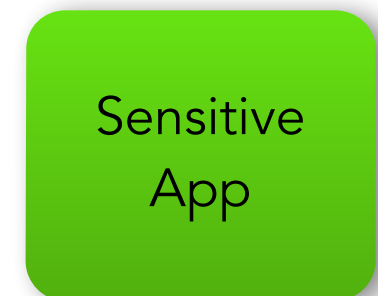
- fault isolation
- fault identification
- IP protection
- modularity

## Trusted next to Untrusted

Untrusted



Trusted



# Isolation is the Key

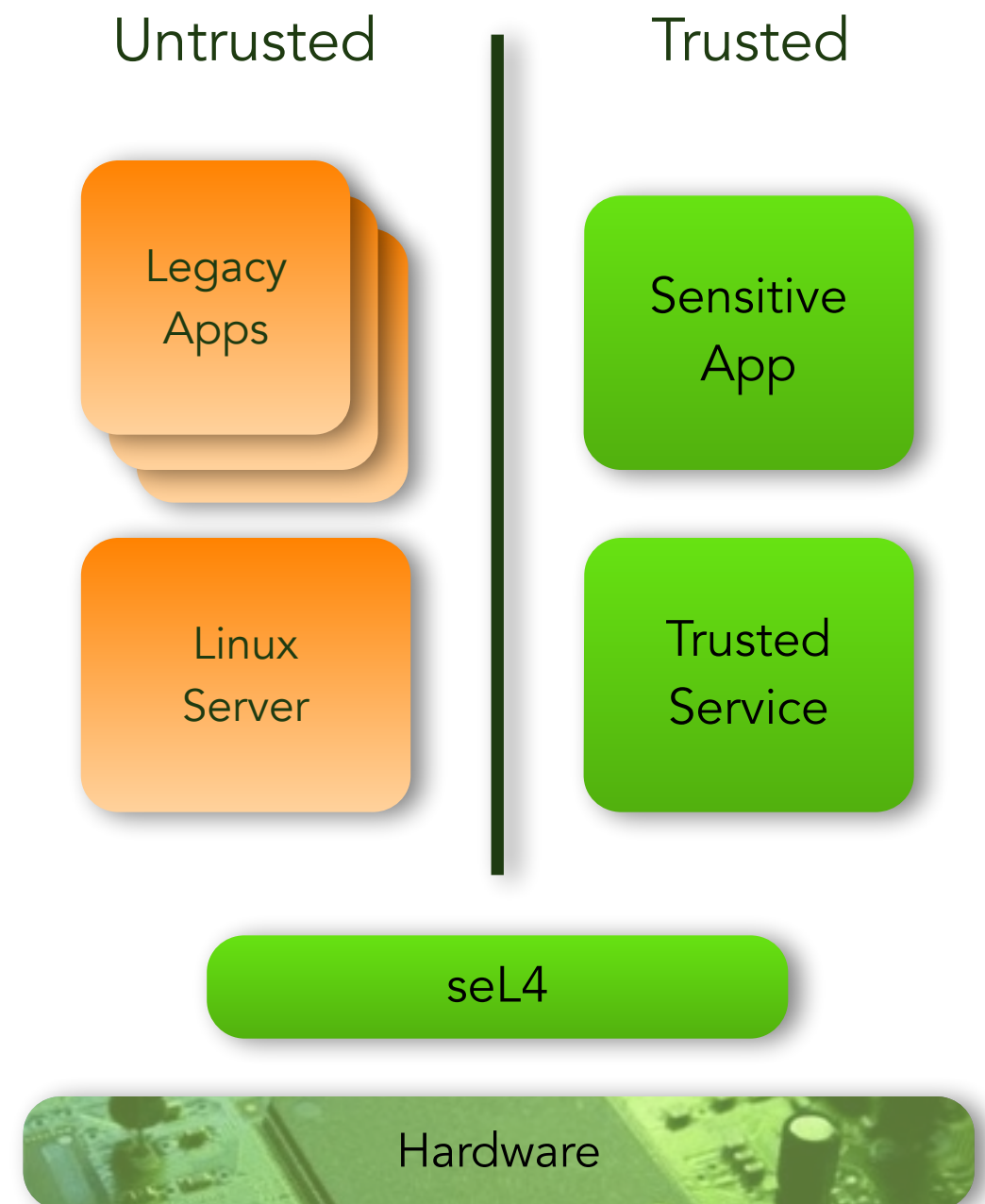
## Trustworthy Computing Base

- message passing
- virtual memory
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- access control

## Applications

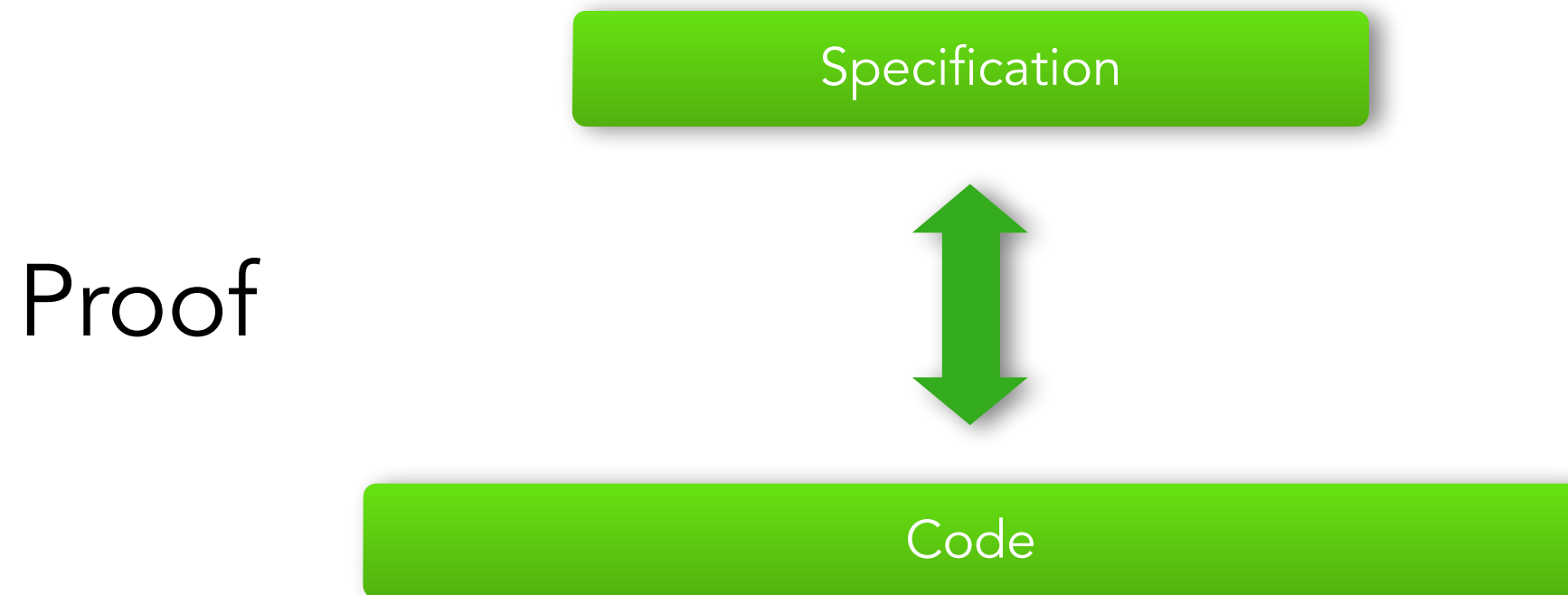
- fault isolation
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- modularity

## Trusted next to Untrusted



# Functional Correctness Possible

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# Functional Correctness Possible

What

Specification

**definition**

```
schedule :: unit s_monad where
schedule ≡ do
  threads ← allActiveTCBs;
  thread ← select threads;
  switch_to_thread thread
od
OR switch_to_idle_thread
```

Proof



Code

# Functional Correctness Possible



What

Specification

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Proof

How

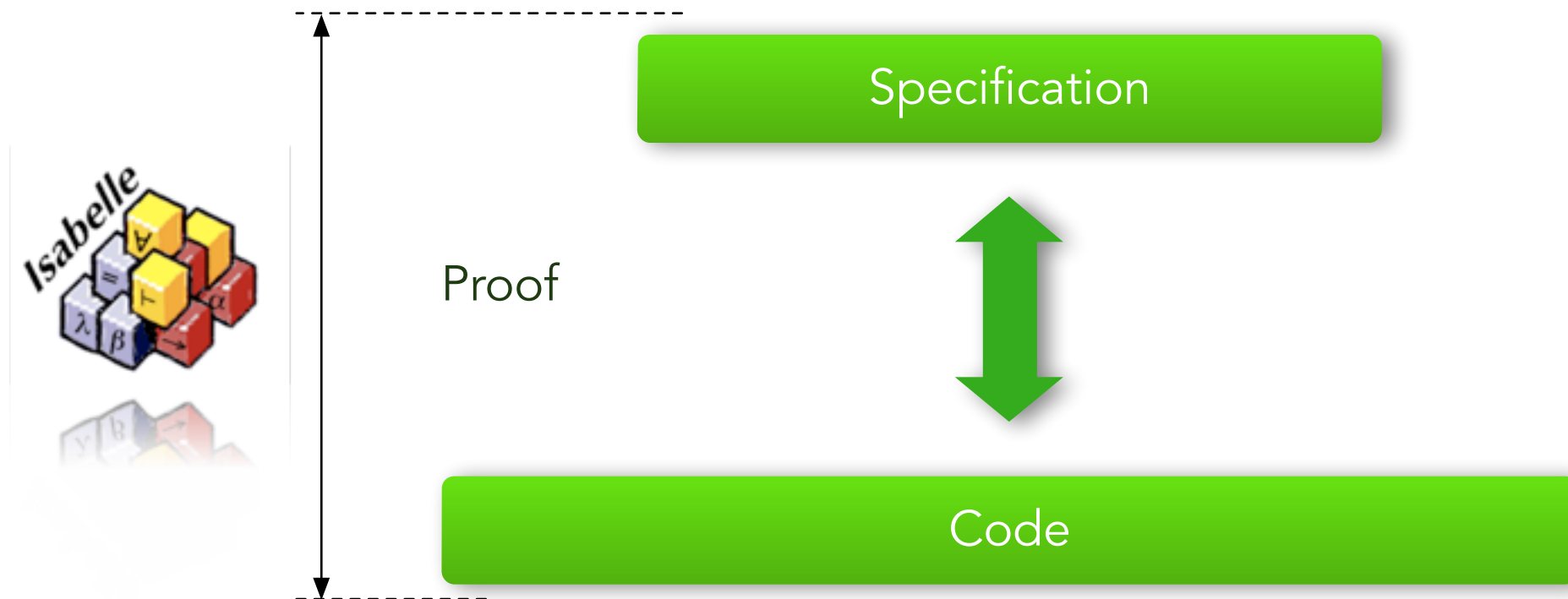
```
void
schedule(void) {
  switch ((word_t)ksSchedulerAction) {
    case (word_t)SchedulerAction_ResumeCurrentThread:
      break;

    case (word_t)SchedulerAction_ChooseNewThread:
      chooseThread();
      ksSchedulerAction = SchedulerAction_ResumeCurrentThread;
      break;

    default: /* SwitchToThread */
      switchToThread(ksSchedulerAction);
      ksSchedulerAction = SchedulerAction_ResumeCurrentThread;
      break;
  }
}

void
chooseThread(void) {
  prio_t prio;
  tcb_t *thread, *next;
```

# \*conditions apply



\*conditions apply



Expectation

Specification

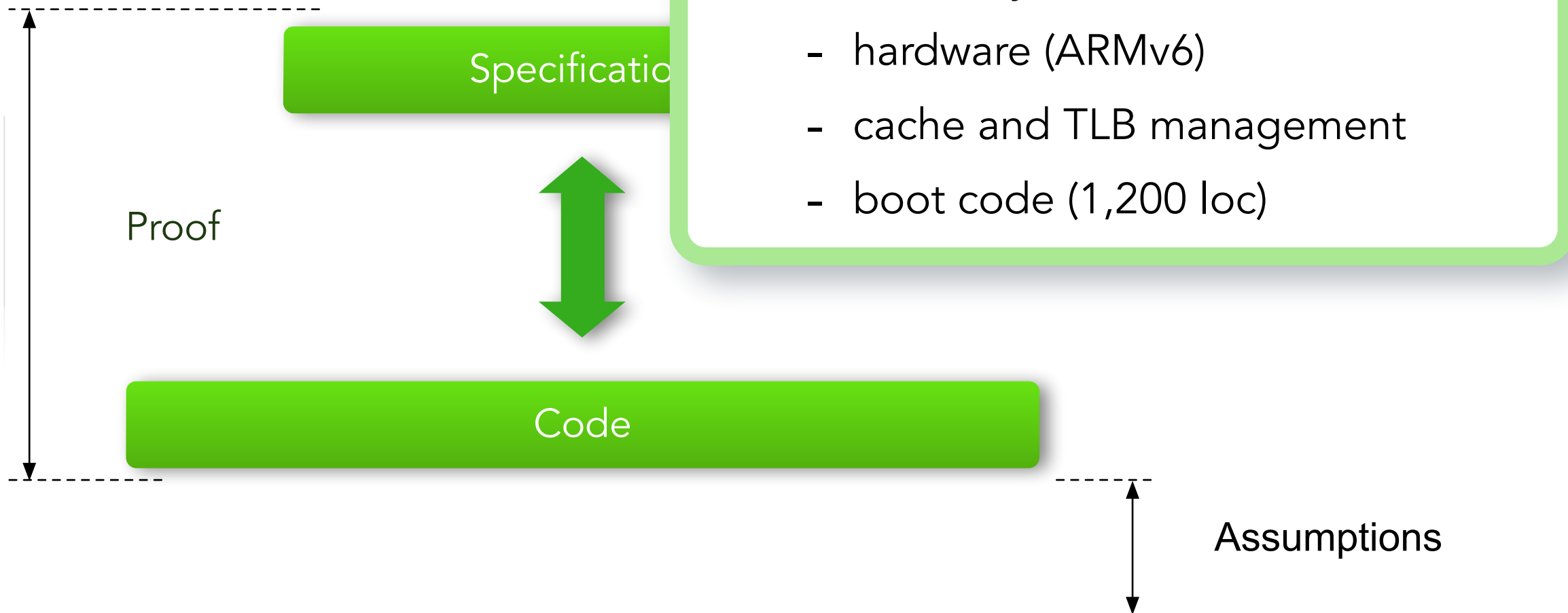
Proof

Code

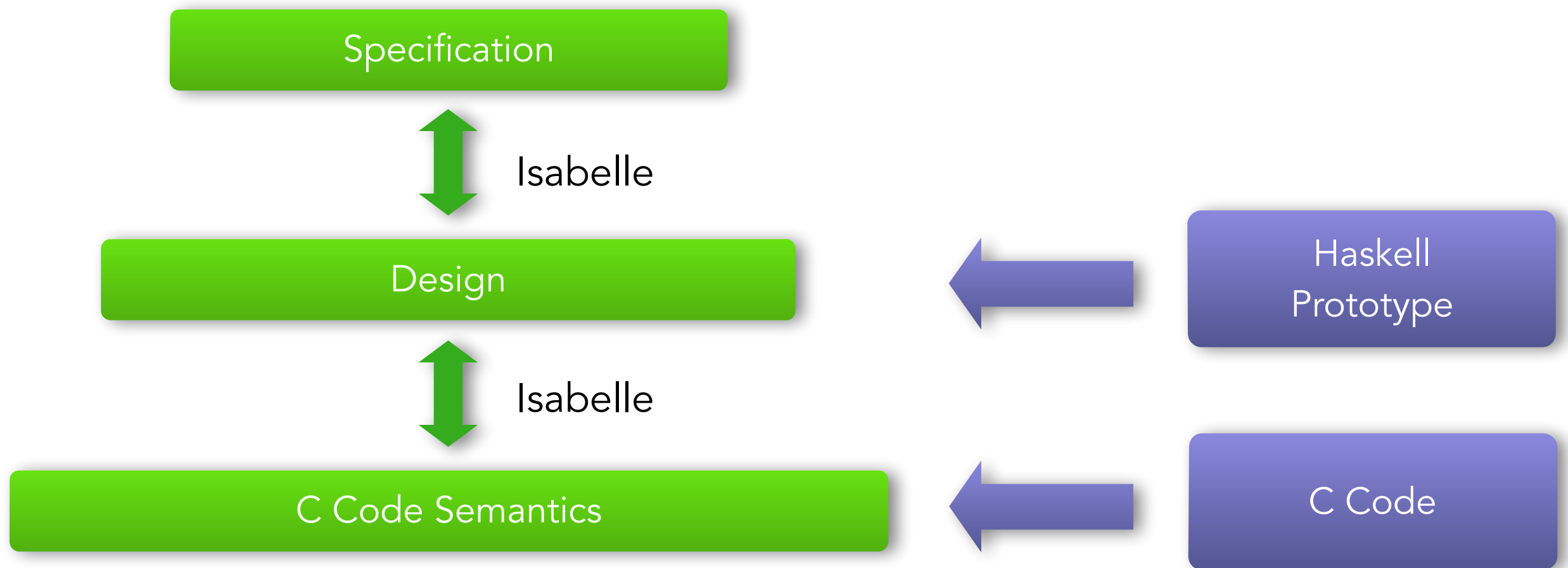
Assumptions



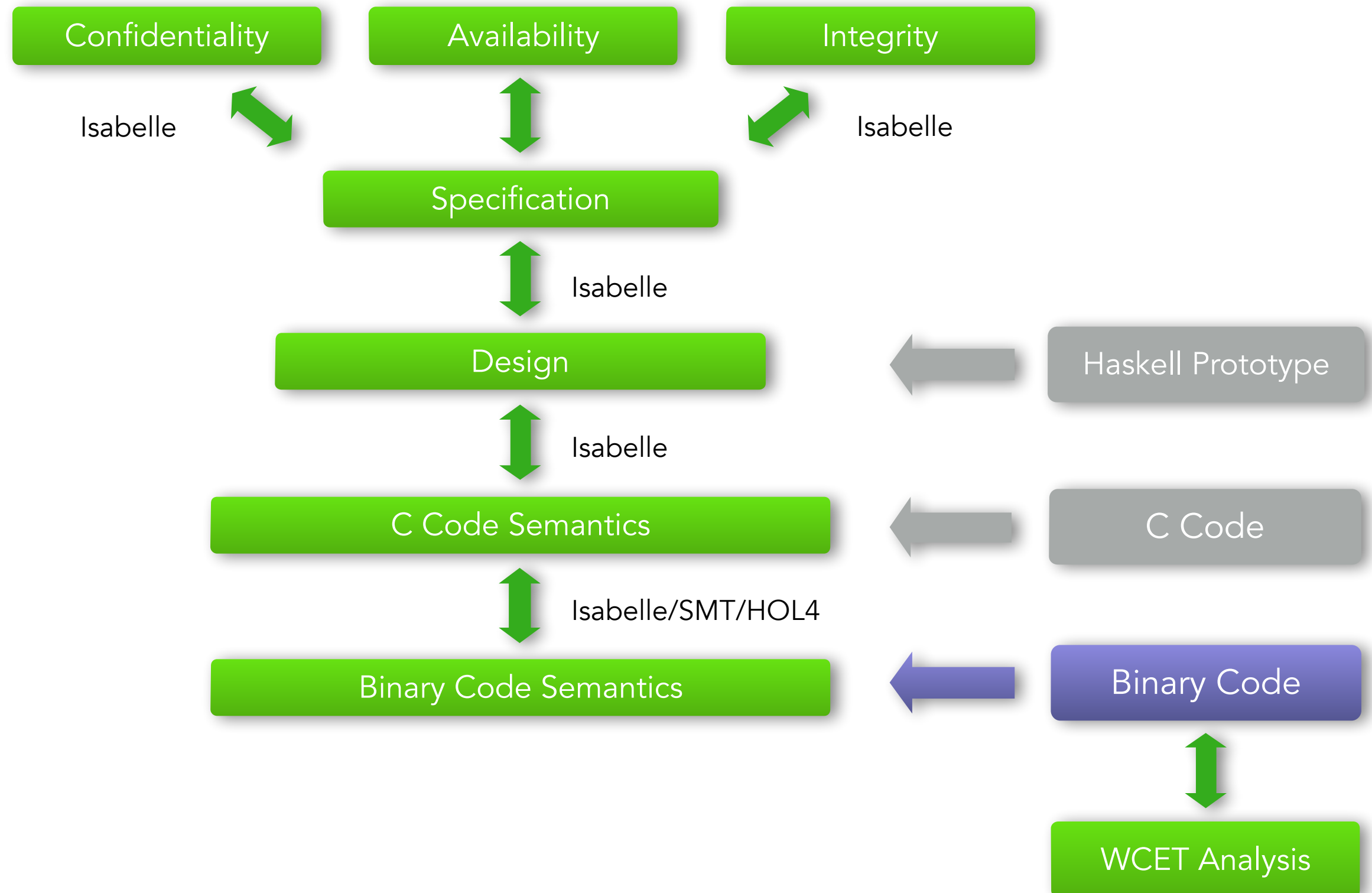
\*conditions apply



# Proof Architecture [SOSP'09]



# Proof Architecture Now



# Proof Architecture Now



## High-level properties:

- functional correctness
- integrity
- authority confinement
- non-interference
- termination
- worst-case execution time (by static analysis)

Isabelle

Isabelle

ntics

Isabelle/SMT/HOL4

antics

Haskell Prototype

C Code

Binary Code

WCET Analysis

# seL4: Unique Assurance

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First and only general-purpose  
OS kernel with full functional-  
correctness proof – at binary level

# seL4: Unique Assurance

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First and only general-purpose  
OS kernel with full functional-  
correctness proof – at binary level

First and only kernel with proof of  
integrity and confidentiality  
enforcement – at binary level

# seL4: Unique Assurance

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First and only general-purpose OS kernel with full functional-correctness proof – at binary level

First and only kernel with proof of integrity and confidentiality enforcement – at binary level

World's fastest microkernel on ARM architecture

# seL4: Unique Assurance

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First and only general-purpose OS kernel with full functional-correctness proof – at binary level

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Predecessor deployed on 2 billion devices

# seL4: Unique Assurance

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First and only general-purpose OS kernel with full functional-correctness proof – at binary level

First and only kernel with proof of integrity and confidentiality enforcement – at binary level

World's fastest microkernel on ARM architecture

Predecessor deployed on 2 billion devices

First and only protected-mode operating-system with complete and sound timing analysis

# seL4: Unique Assurance



First and only general-purpose  
OS kernel with full  
correctness proof –

First and only kernel with proof of  
and confidentiality  
– at binary level

## Open Source

29 July 2014

World's fastest microprocessor  
ARM architecture

processor deployed on  
2 billion devices

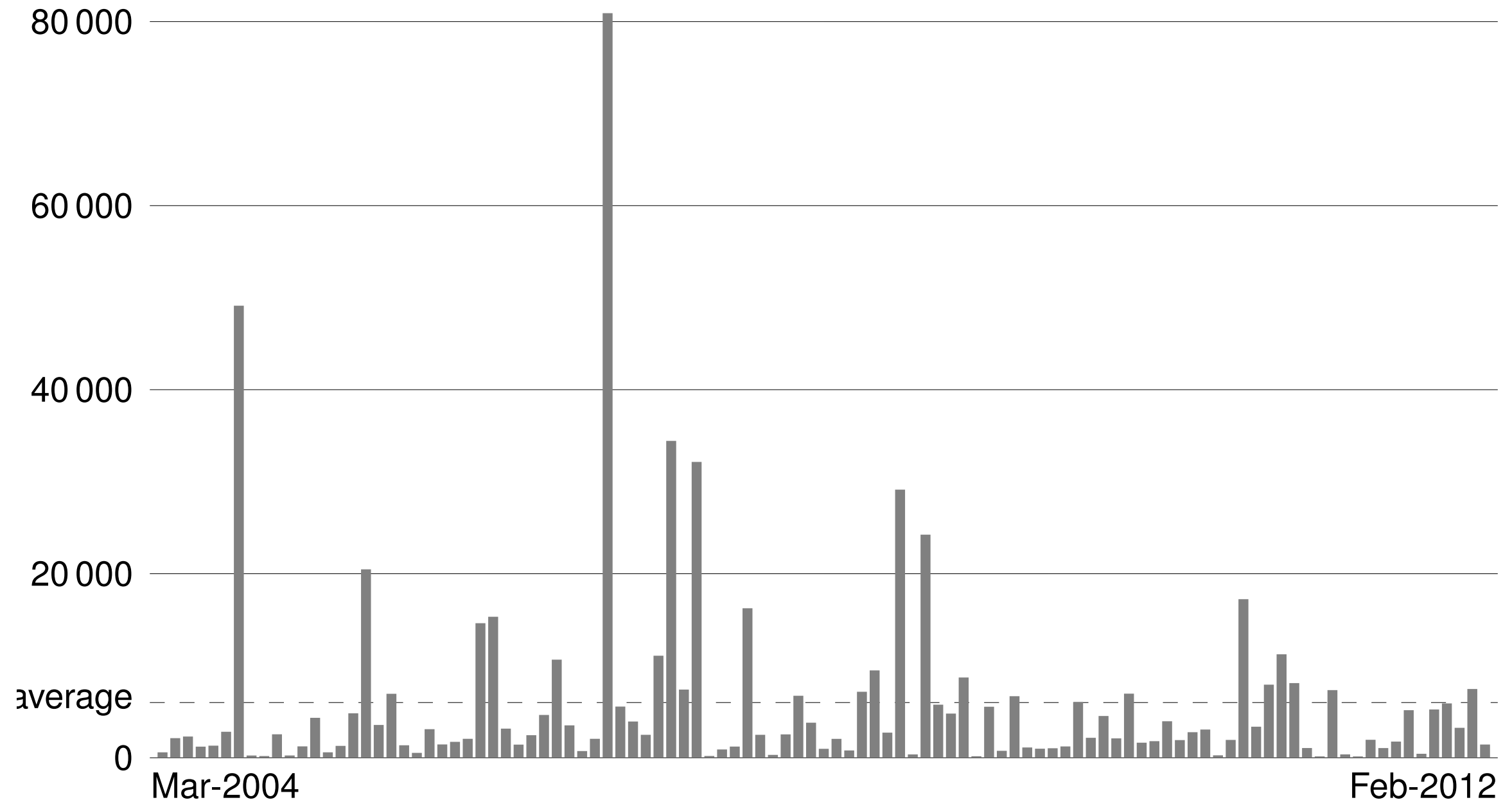
First and only protected-mode  
operating-system with complete  
and sound timing analysis

# Scale



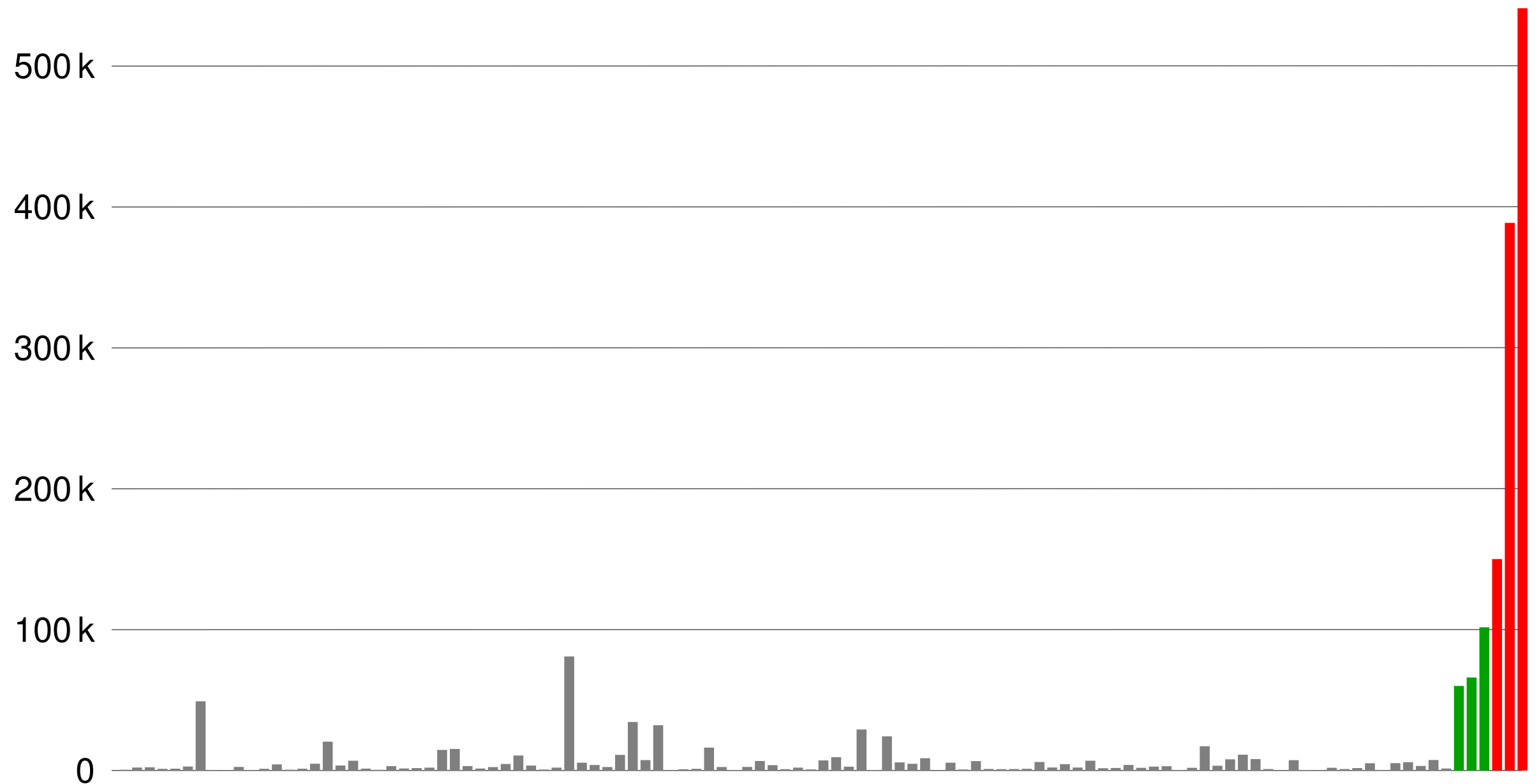
**Scale**

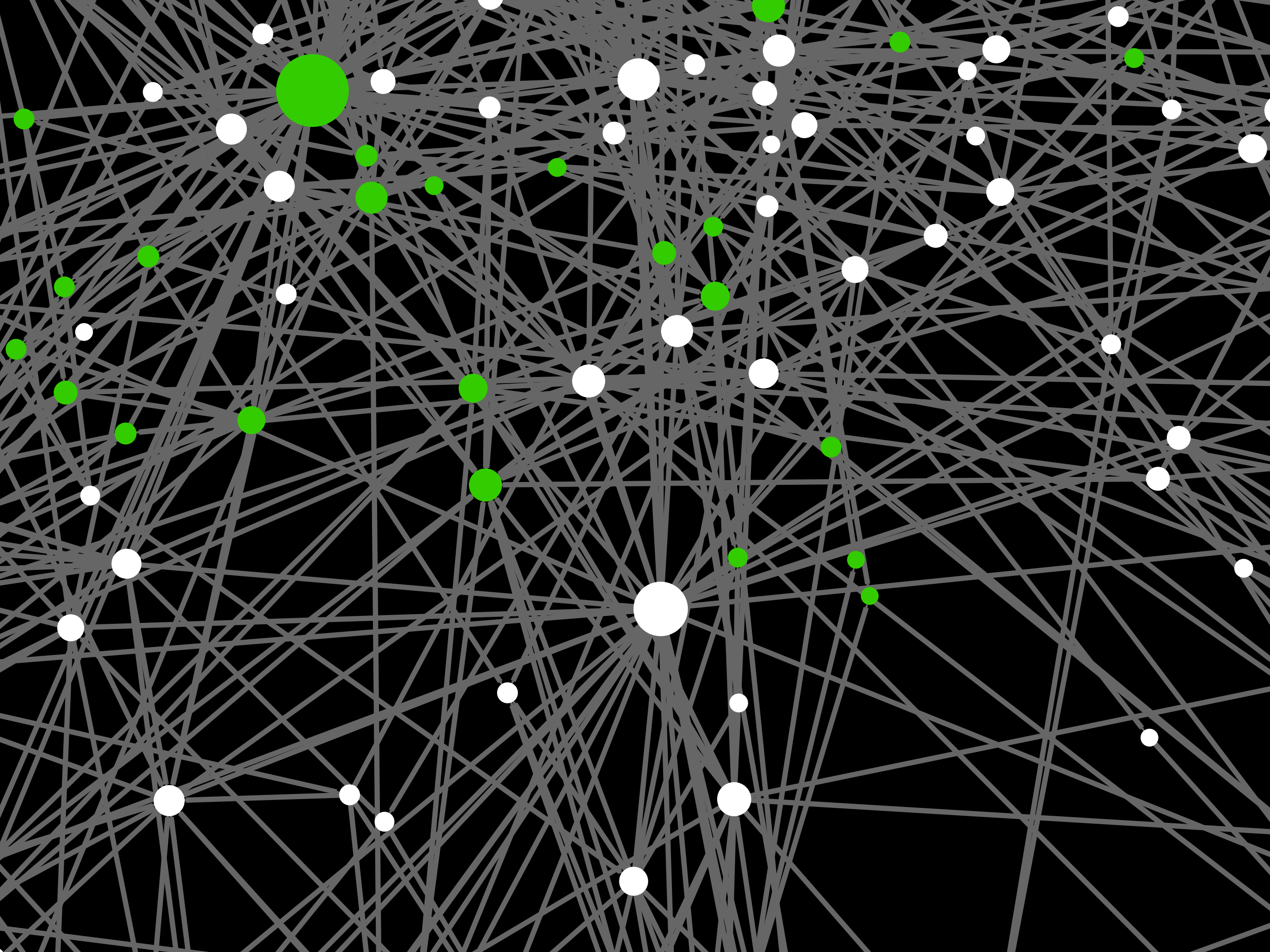
# Scale



Size distribution of AFP entries in lines of proof,  
sorted by submission date

# Scale







seL4

# Demo